

Notice of References CitedApplication No.
09/530,588Applicant(s)
Matsuse et al.Examiner
Quach, T.Group Art Unit
2814

Page 1 of 1

U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A	6,084,279	7/2000	Nguyen et al.	257	412
B	5,796,166	8/1998	Agnello et al.	257	751
C	5,134,451	7/1992	Katoh	257	412
D	6,096,630	8/2000	Byun et al.	438	592
E	5,962,904	10/1999	Hu	257	412
F	6,103,607	8/2000	Kizilayalli et al.	438	588
G	6,060,361	5/2000	Lee	438	283
H					
I					
J					
K					
L					
M					

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
N						
O						
P						
Q						
R						
S						
T						

NON-PATENT DOCUMENTS

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
U	Wolf, S., Silicon Processing for the VLSI Era, Vol. 2, Lattice Press, 1990, pp. 192-193, 587-591.	1990
V		
W		
X		